



Product Specification

M200RW01 V3 open cell

AU OPTRONICS CORPORATION

() Preliminary Specification

(V) Final Specification

Module	20.0" Color TFT-LCD
Model Name	M200RW01 V3 Open Cell

Customer	Date
Approved by	
Note: This Specification is subject to change without notice.	

Approved by	Date
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Desktop Display Business Group / AU Optronics corporation	



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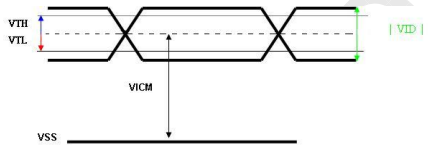
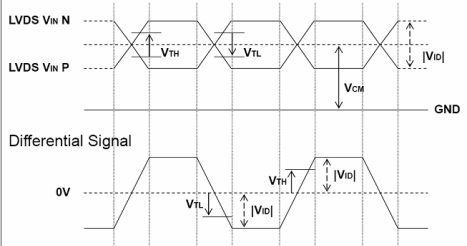
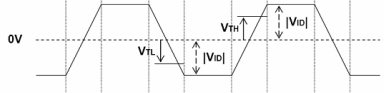


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Record of Revision

Version and Date	Page	Old description	New Description	Remarks																																				
0.1 2009/11/16	All	First Edition for Customer	N/A																																					
0.2 2010/01/22	6	2.2 Optical Characteristics <table><tr><th>Item</th><th>Unit</th><th>Conditions</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Note</th></tr><tr><td rowspan="2">Viewing Angle</td><td rowspan="2">[degree]</td><td>Horizontal (Right) CR = 10</td><td>70</td><td>90</td><td></td><td rowspan="2">2</td></tr><tr><td>Vertical (Up) CR = 10</td><td>45</td><td>65</td><td></td></tr></table>	Item	Unit	Conditions	Min.	Typ.	Max.	Note	Viewing Angle	[degree]	Horizontal (Right) CR = 10	70	90		2	Vertical (Up) CR = 10	45	65		2.2 Optical Characteristics <table><tr><th>Item</th><th>Unit</th><th>Conditions</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Note</th></tr><tr><td rowspan="2">Viewing Angle</td><td rowspan="2">[degree]</td><td>Horizontal (Right) CR = 10</td><td>40</td><td>45</td><td></td><td rowspan="2">2</td></tr><tr><td>Vertical (Up) CR = 10</td><td>15</td><td>20</td><td></td></tr></table>	Item	Unit	Conditions	Min.	Typ.	Max.	Note	Viewing Angle	[degree]	Horizontal (Right) CR = 10	40	45		2	Vertical (Up) CR = 10	15	20		
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	10	3.0 Functional Block Diagram I/F PCB Interface: MSCKT2407P30HB	3.0 Functional Block Diagram I/F PCB Interface: AL230F-A0G1D-P (P-TWO) MSCKT2407P30HB (STM)																																					
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	14	5.2 Backlight Unit CCFL Ignition Voltage (ViCFL, Ta= 0□) Max. 1620 [Volt] rms CCFL Ignition Voltage (ViCF, Ta= 25□) Max. 1240 [Volt] rms	5.2 Backlight Unit CCFL Ignition Voltage (ViCFL, Ta= 0□) Max. 1660 [Volt] rms CCFL Ignition Voltage (ViCF, Ta= 25□) Max. 1280 [Volt] rms																																					
	20	6.6 Power ON/OFF Sequence T1 value: Min. 0.5msec / Max. 10msec	6.6 Power ON/OFF Sequence T1 value: Min. 0.2msec / Max. 1msec																																					
	21	7.1 TFT LCD Module Type Part Number: MSCKT2407P30HB	7.1 TFT LCD Module Type Part Number: AL230F-A0G1D-P (P-TWO) MSCKT2407P30HB (STM)																																					
1.0 2010/04/22	25 26	10.0 Mechanical Characteristics.	10.0 Mechanical Characteristics. Revised 2D drawing																																					
	All	N/A	Final Spec																																					
20100722	Sec 10	Old panel diagram	New with COF position																																					



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2010/12/15	20	6.6 Power ON/OFF Sequence Parameter T1 max=1msec	6.6 Power ON/OFF Sequence Parameter T1 max=10msec	
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1.0 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the CCFL reflector edge. Instead, press at the far ends of the CCFL Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- 13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 14) The LCD module is designed so that the CCFL in it is supplied by Limited Current Circuit (IEC60950 or UL1950). Do not connect the CCFL in Hazardous Voltage Circuit.



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2.0 General Description

This specification applies to the 20 inch-wide Color a-Si TFT-LCD Module M200RW01. The display supports the HD+ 1600(H) x 900(V) screen format and 16.7M colors (RGB 6-bits + Hi-FRC data). All input signals are 2-channel LVDS interface and this module doesn't contain an inverter board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25℃ condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	508.05 (20.0")
Active Area	[mm]	442.8 (H) x 249.075 (V)
Pixels H x V		1600(x3) x 900
Pixel Pitch	[um]	276.75 (per one triad) ×276.75
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN Mode, Normally White
Contrast Ratio		700 (Typ.)
Optical Response Time	[msec]	5ms (Typ., on/off)
Nominal Input Voltage VDD	[Volt]	+5.0 V
Power Consumption (VDD line)	[Watt]	5 (Typ.)
Cell Weight	[Grams]	417 g
Electrical Interface		Dual channel LVDS
Support Color		16.7M colors (RGB 6-bit + Hi_FRC)
Surface Treatment		Anti-Glare, 3H
Cell transmittance		6.8%
Cell thickness		1.4 mm



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2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C:

Item	Unit	Conditions	Min.	Typ.	Max.	Note
Response Time	[msec]	Raising Time (T_{rR})	-	3.4	7.4	1,2
		Falling Time (T_{rF})	-	1.6	2.6	
		Raising + Falling	-	5	10	
Crosstalk (in 60Hz)	[%]				1.5	3
Flicker	dB				-20	4



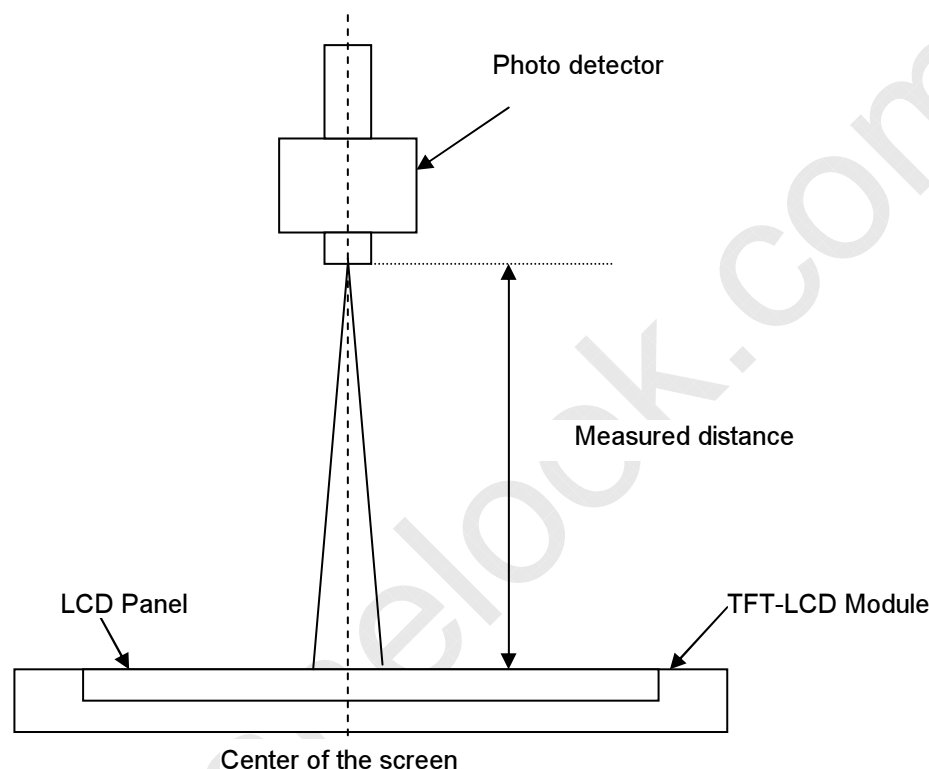
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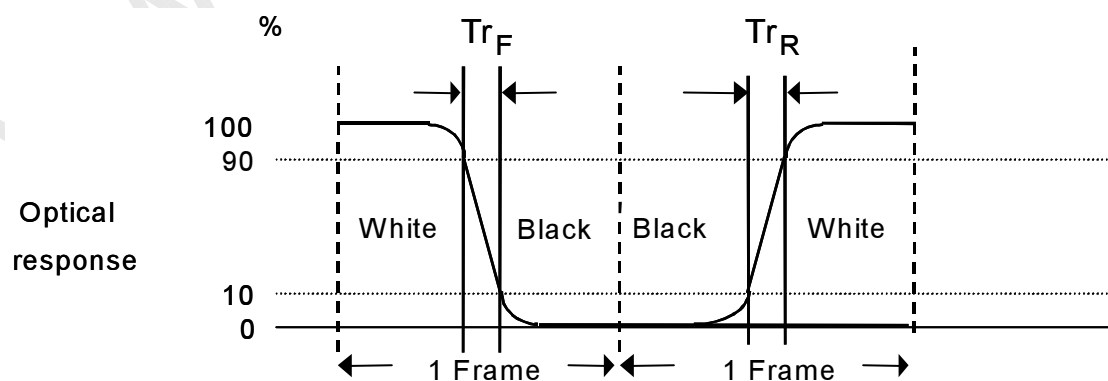
Note 1: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35℃). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.



Note 2: Definition of Response time measured by Westar TRD-100A

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time, Tr_R), and from "Full White" to "Full Black" (falling time, Tr_F), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) of amplitudes.



$$Tr_R + Tr_F = 5 \text{ msec (typ.)}$$



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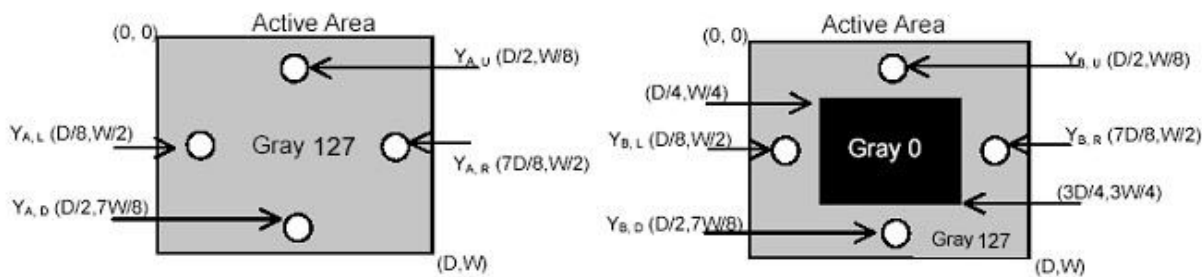
Note 3: Crosstalk is defined as below and measured by TOPCON SR-3

$$CT = |Y_B - Y_A| / Y_A \times 100 (\%)$$

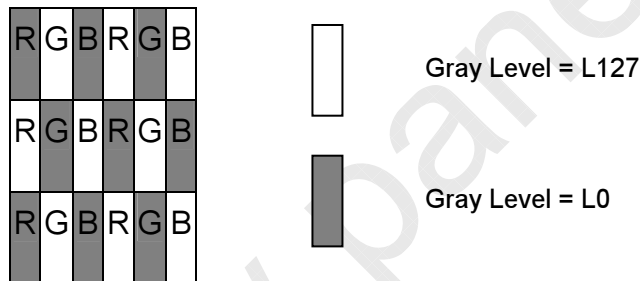
Where

Y_A = Luminance of measured location without gray level 0 pattern (cd/m²)

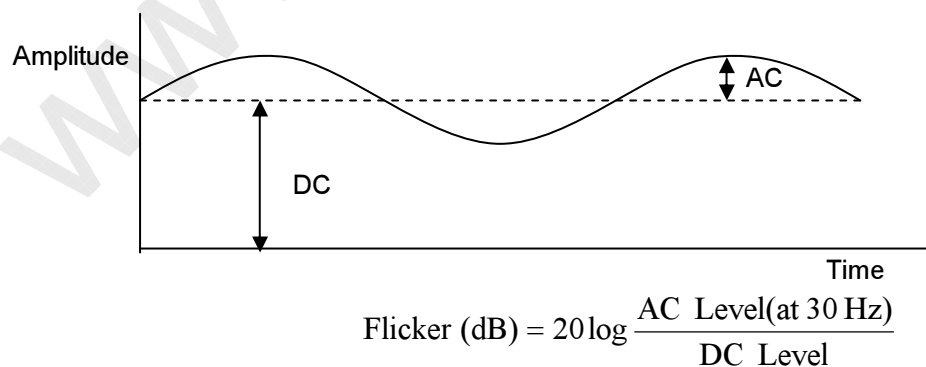
Y_B = Luminance of measured location with gray level 0 pattern (cd/m²)



Note 4: Test Pattern: Subchecker Pattern measured by TOPCON SR-3



Method: Record dBV & DC value with TRD-100





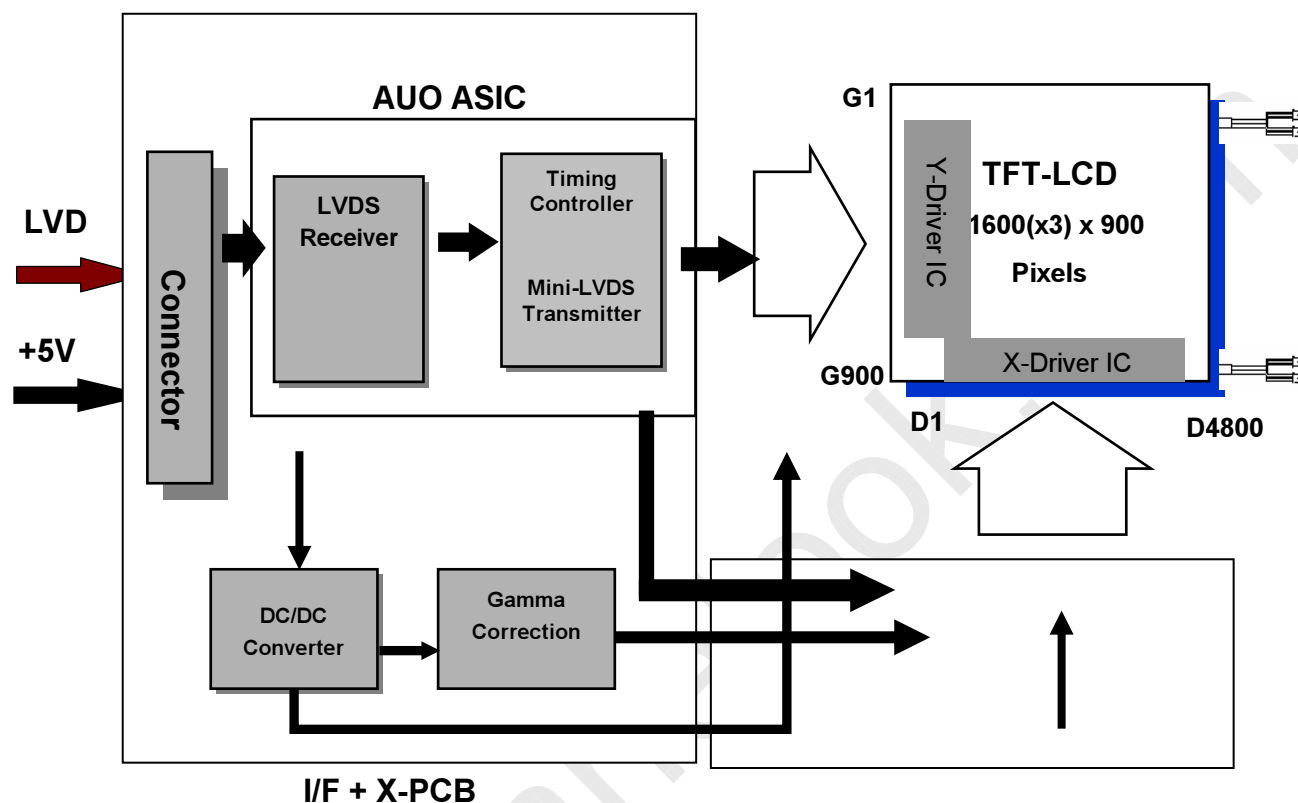
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3.0 Functional Block Diagram

The following diagram shows the functional block of the 20.0 inch Color TFT-LCD Open Cell.:



I/F PCB Interface:

AL230F-A0G1D-P (P-TWO)

MSCKT2407P30HB (STM)

Mating Type:

P2407P30 (Unlocked type)

PK2407P30 (Locked type)



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4.0 Absolute Maximum Ratings

Absolute maximum ratings of the module are as following:

4.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	0	5.5	[Volt]	Note 1,2

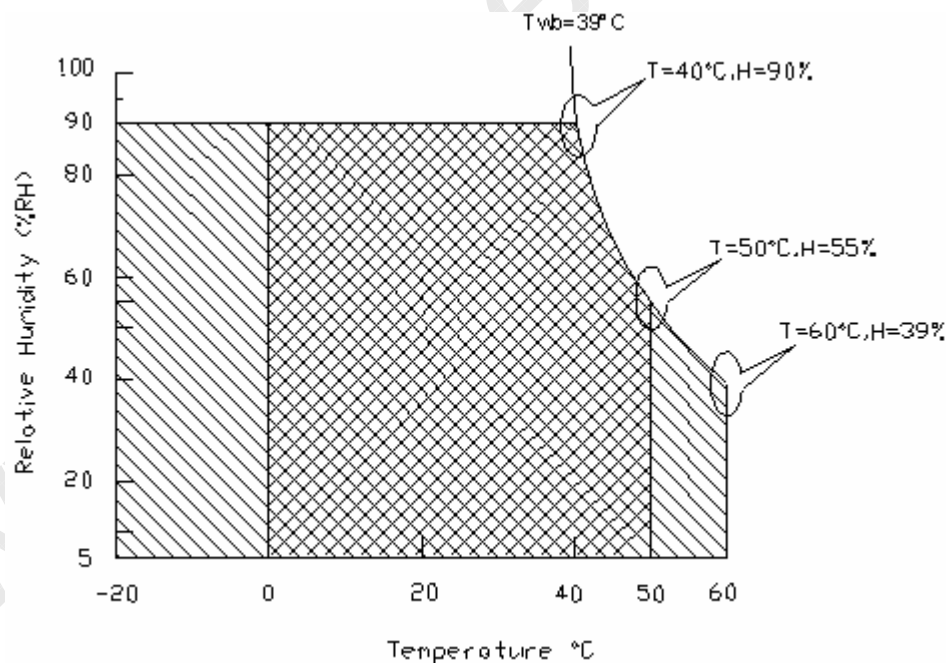
4.2 Absolute Ratings of Environment

Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[°C]	Note 3
Operation Humidity	HOP	5	90	[%RH]	
Storage Temperature	TST	-20	+60	[°C]	
Storage Humidity	HST	5	90	[%RH]	

Note 1: With in Ta (25□)

Note 2: Permanent damage to the device may occur if exceeding maximum values

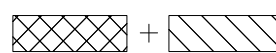
Note 3: For quality performance, please refer to AUO IIS(Incoming Inspection Standard).



Operating Range



Storage Range





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5.0 Electrical characteristics

5.1 TFT LCD Module

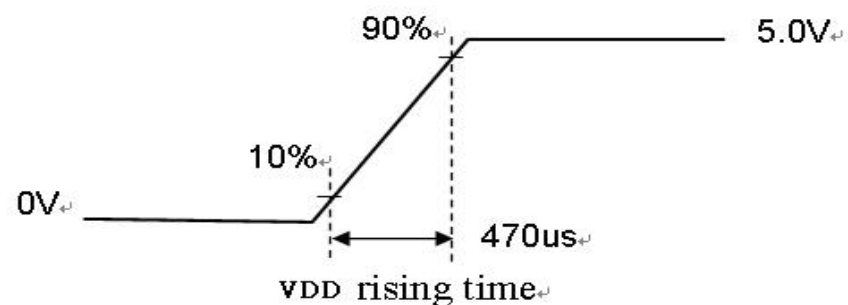
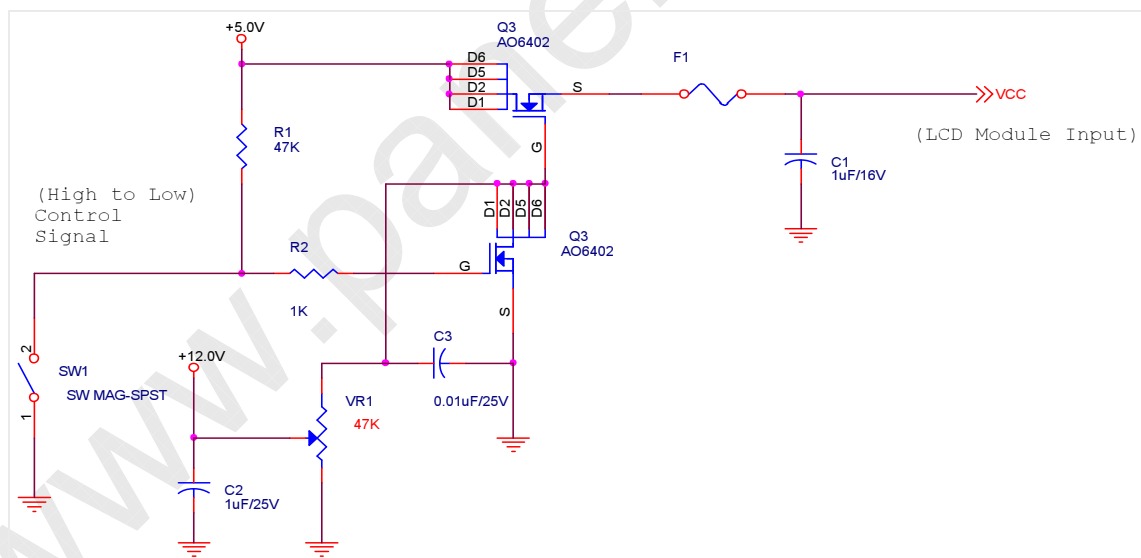
5.1.1 Power Specification

Input power specifications are as following:

Symbol	Parameter	Min	Typ	Max	Unit	Conditions
VDD	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	+/-10%
IDD	Input Current	-	1.00	1.18	[A]	VDD= 5.0V, All Black Pattern At 60Hz, +30%
PDD	VDD Power	-	5.00	5.90	[Watt]	VDD= 5.0V, All Black Pattern At 60Hz
IRush	Inrush Current	-	-	3	[A]	Note 1
VDDrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	300	[mV] p-p	VDD= 5.0V, All Black Pattern At 60Hz

Note 1: Measurement conditions:

The duration of rising time of power input is 470us.





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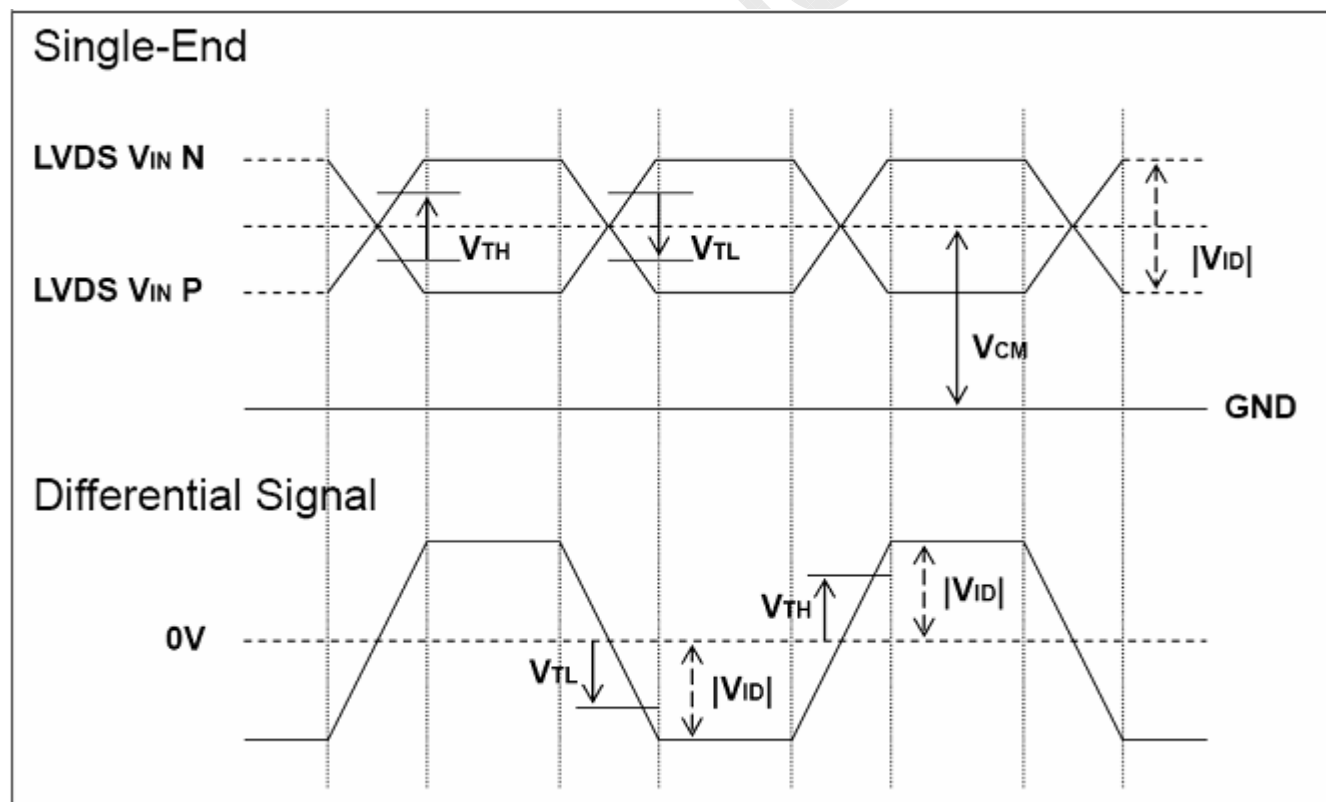
5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off. Please refer to specifications of SN75LVDS82DGG (Texas Instruments) in detail.

Characteristics of each signal are as following:

Symbol	Parameter	Min	Typ	Max	Units	Condition
V _{TH}	Differential Input High Threshold	-	+50	+100	[mV]	V _{ICM} = 1.2V Note 1
V _{TL}	Differential Input Low Threshold	-100	-50	-	[mV]	V _{ICM} = 1.2V Note 1
V _{ID}	Input Differential Voltage	100	-	600	[mV]	Note 1
V _{ICM}	Differential Input Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V _{TH} -V _{TL} = 200mV (max) Note 1

Note 1: LVDS Signal Waveform





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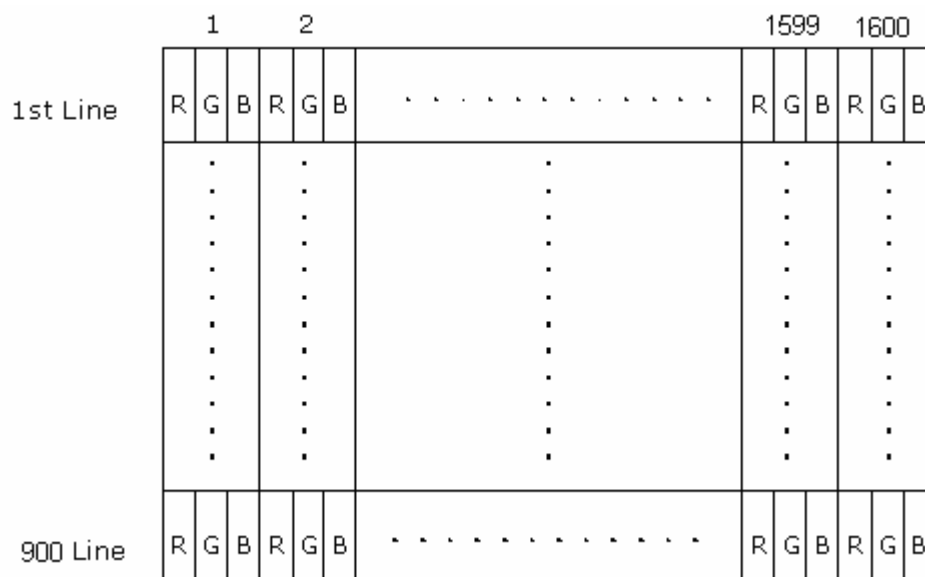
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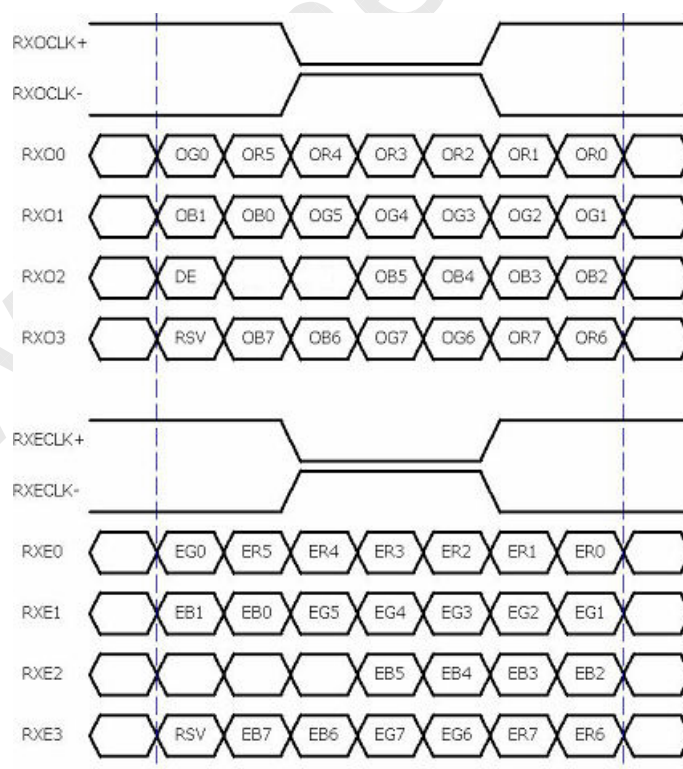
6.0 Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.



6.2 The input data format



Note 1: R/G/B data 7:MSB, R/G/B data 0:LSB O = "First Pixel Data" E = "Second Pixel Data"



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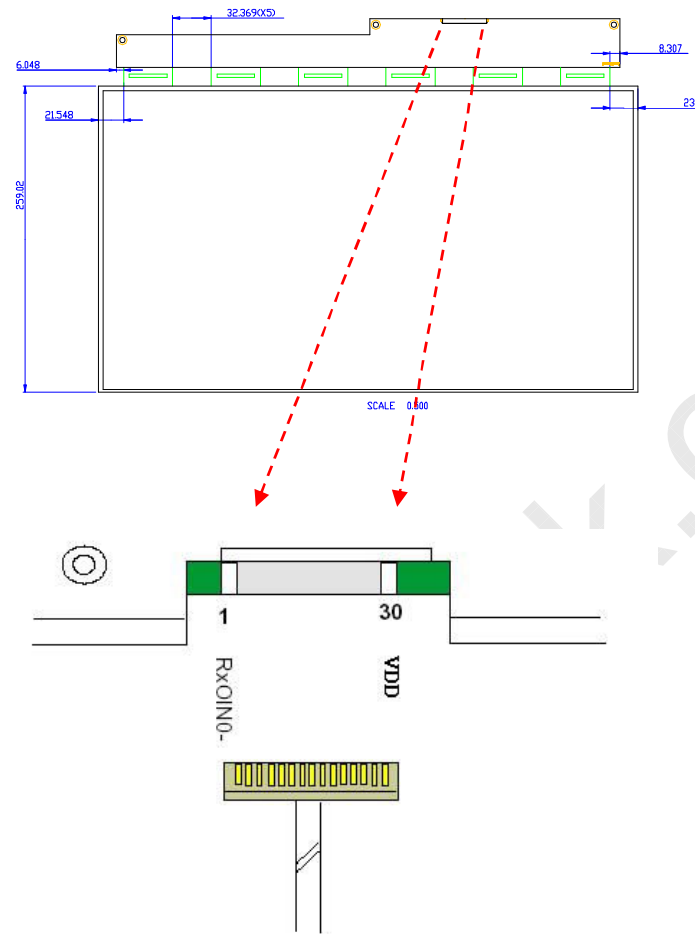
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6.3 Signal Description

The module using one LVDS receiver SN75LVDS82(Texas Instruments). LVDS is a differential signal technology for LCD interface and high speed data transfer device. LVDS transmitters shall be SN75LVDS83(negative edge sampling). The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

PIN #	SIGNAL NAME	DESCRIPTION
1	RxOIN0-	Negative LVDS differential data input (Odd data)
2	RxOIN0+	Positive LVDS differential data input (Odd data)
3	RxOIN1-	Negative LVDS differential data input (Odd data)
4	RxOIN1+	Positive LVDS differential data input (Odd data)
5	RxOIN2-	Negative LVDS differential data input (Odd data, DSPTMG)
6	RxOIN2+	Positive LVDS differential data input (Odd data, DSPTMG)
7	GND	Power Ground
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)
10	RxOIN3-	Negative LVDS differential data input (Odd data)
11	RxOIN3+	Positive LVDS differential data input (Odd data)
12	RxEIN0-	Negative LVDS differential data input (Even data)
13	RxEIN0+	Positive LVDS differential data input (Even data)
14	GND	Power Ground
15	RxEIN1-	Negative LVDS differential data input (Even data)
16	RxEIN1+	Positive LVDS differential data input (Even data)
17	GND	Power Ground
18	RxEIN2-	Negative LVDS differential data input (Even data)
19	RxEIN2+	Positive LVDS differential data input (Even data)
20	RxECLK-	Negative LVDS differential clock input (Even clock)
21	RxECLK+	Positive LVDS differential clock input (Even clock)
22	RxEIN3-	Negative LVDS differential data input (Even data)
23	RxEIN3+	Positive LVDS differential data input (Even data)
24	GND	Power Ground
25	NC	No connection (for AUO test only. Do not connect)
26	NC	No connection (for AUO test only. Do not connect)
27	NC	No connection (for AUO test only. Do not connect)
28	VDD	Power +5V
29	VDD	Power +5V
30	VDD	Power +5V

Note1: Start from left side



Note2: Input signals of odd and even clock shall be the same timing.

6.4 Timing Characteristics

Basically, interface timing described here is not actual input timing of LCD module but close to output timing of SN75LVDS82DGG (Texas Instruments) or equivalent.

Item		Symbol	Min	Typ	Max	Unit
Data CLK		1/Tclk	43.2	60.5	80	[MHz]
H-section	Period	Th	950	1080	2047	[Tclk]
	Display Area	Tdisp(h)	800	800	800	[Tclk]
	Blanking	Tblk(h)	150	280	-	[Tclk]
V-section	Period	Tv	910	934	2047	[Th]
	Display Area	Tdisp(v)	900	900	900	[Th]
	Blanking	Tblk(v)	10	34	-	[Th]
Frame Rate		F	50	60	75	[Hz]



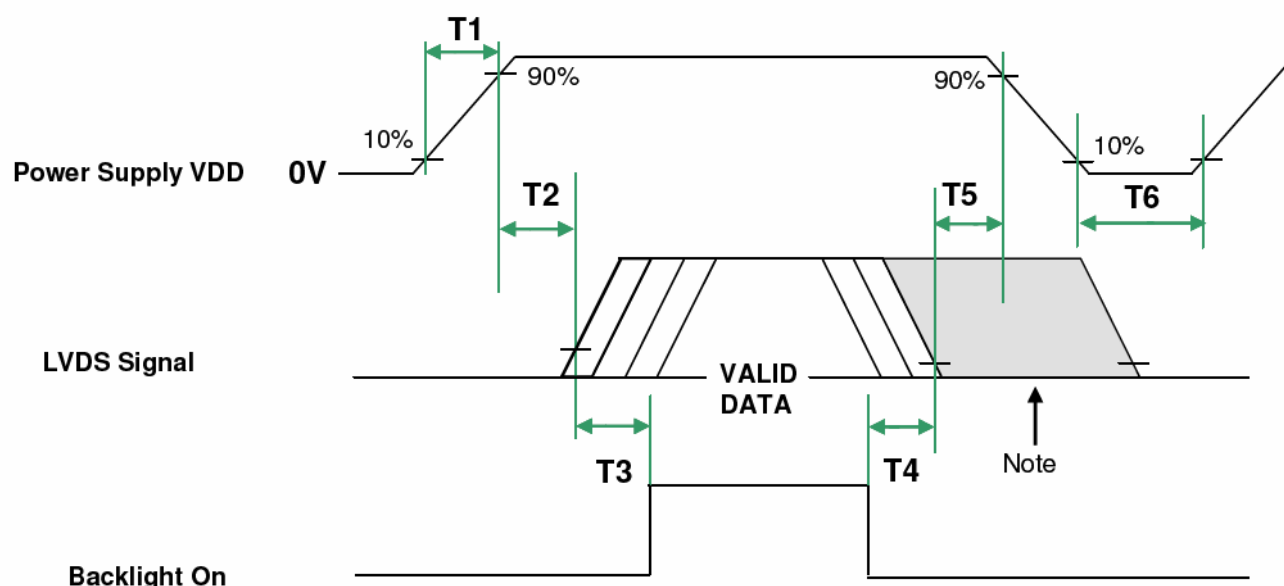
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6.6 Power ON/OFF Sequence

VDD power and lamp on/off sequence are as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Note : Insert a white pattern after valid data and last until VDD falls to 10%

Parameter	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	-	10	[msec]
T2	0	-	50	[msec]
T3	500	-		[msec]
T4	200	-	-	[msec]
T5	40	-	-	[msec]
T6	1000	-	-	[msec]



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7.0 Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	Interface Connector / Interface card
Manufacturer	STM / P-TWO
Type Part Number	AL230F-A0G1D-P (P-TWO) MSCKT2407P30HB (STM)
Mating Housing Part Number	P2407P30 (Unlocked type) PK2407P30 (Locked type)

7.1.1 Pin Assignment

Pin#	Signal Name	Pin#	Signal Name
1	RxOIN0-	2	RxOIN0+
3	RxOIN1-	4	RxOIN1+
5	RxOIN2-	6	RxOIN2+
7	GND	8	RxOCLKIN-
9	RxOCLKIN+	10	RxOIN3-
11	RxOIN3+	12	RxEIN0-
13	RxEIN0+	14	GND
15	RxEIN1-	16	RxEIN1+
17	GND	18	RxEIN2-
19	RxEIN2+	20	RxECLKIN-
21	RxECLKIN+	22	RxEIN3-
23	RxEIN3+	24	GND
25	NC (for AUO test only. Do not connect)	26	NC (for AUO test only. Do not connect)
27	NC (for AUO test only. Do not connect)	28	VDD
29	VDD	30	VDD



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8.0 Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50□, 80%RH, 300hours	
High Temperature Storage (HTS)	Ta= 60□, 300hours	
Low Temperature Storage (LTS)	Ta= -20□, 300hours	
Thermal Shock Test (TST)	-20□/30min, 60□/30min, 100 cycles	1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	2
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20℃ to 60℃, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: EN61000-4-2, ESD class B: Certain performance degradation allowed

No data lost

Self-recoverable

No hardware failures.



9.0 Mechanical Characteristics

